		
Application No.	Applicant(s)	
10/731,312	MCELVAIN ET AL.	
Examiner	Art Unit	
Leigh Marie Garbowski	2825	
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<u>04/28/2004</u> .		
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6. ☐ Interview Summary Paper No./Mail Dat	(PTO-413), te)-152)
8. ⊠ Examiner's Stateme	ent of Reasons for Allo	wance
	Leigh Marie Garbowski ars on the cover sheet with the co (OR REMAINS) CLOSED in this ap or other appropriate communication GHTS. This application is subject to and MPEP 1308. M/28/2004. the Examiner. der 35 U.S.C. § 119(a)-(d) or (f). been received. been received in Application No cuments have been received in this of this communication to file a reply ENT of this application. itted. Note the attached EXAMINER as reason(s) why the oath or declara to be submitted. on's Patent Drawing Review (PTO- as Amendment / Comment or in the Co and AMERIAL representation of BIOLOGICAL MATERIAL rep	Examiner Leigh Marie Garbowski 2825 ars on the cover sheet with the correspondence addressing the cover sheet with the correspondence addressing to the paper of the paper of the paper of the mailed in due of GHTS. This application is subject to withdrawal from issue and MPEP 1308. MAZ8A2004. The Examiner. Index 35 U.S.C. § 119(a)-(d) or (f). Index application No

Allowable Subject Matter

Claims 1 and 138-181 are allowed.

The following is an examiner's statement of reasons for allowance: although the prior art of record cited below discloses partitioning a RTL netlist between a plurality of ICs, the prior art of record does not disclose or teach or provide sufficient motivation for partitioning a technology independent RTL netlist between a plurality of ICs which includes a first IC to be implemented in a first vendor specific technology and a second IC to be implemented in a second vendor specific technology.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. W.-J. FANG et al. disclose partitioning a design onto a set of FPGAs. E.D. LAGNESE et al. disclose architectural partitioning, including mention that the partitions can be implemented on off-the-shelf microprocessors. M. VOOTUKURU et al. disclose partitioning RTL designs on multiple FPGAs, including mention that the system can be extended to heterogeneous FPGA targeting. However, none of the references adequately describe the plurality of ICs to be implemented in first and second vendor specific technologies.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leigh Marie Garbowski whose telephone number is 571-272-1893. The examiner can normally be reached on days.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on 571-272-1907. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

LEIGH M. GARBOWSKI PRIMARY EXAMINER